



40V, 1A, Low IQ, Synchronous DC-DC Buck Converter with Internal Compensation and Programmable Soft Start

DESCRIPTION

The AP64102Q is an automotive-compliant, 1A, synchronous buck converter with a wide input voltage range of 3.8V to 40V. The device fully integrates a 150m Ω high-side power MOSFET and a 80m Ω low-side power MOSFET to provide high-efficiency step-down DC-DC conversion.

The AP64102Q device is easily used by minimizing the external component count due to its adoption of peak current mode control along with its integrated loop compensation network.

The AP64102Q design is optimized for Electromagnetic Interference (EMI) reduction. The device has a proprietary gate driver scheme

FEATURES

- AEC-Q100 Qualified
- Wide Input Range: 3.8V to 40V
- 1A Continuous Output Current
- 0.8V ±1% Reference Voltage
- 25µA Ultralow Quiescent Current (Pulse Frequency Modulation)
- Programmable Switching Frequency: 100kHz to 2.2MHz
- External Clock Synchronization: 100kHz to 2.2MHz
- Adjustable Soft-Start Time
- Proprietary Gate Driver Design for Best EMI Reduction
- Frequency Spread Spectrum (FSS) to Reduce EMI

to resist switching node ringing without sacrificing MOSFET turn-on and turn-off times, which reduces high-frequency radiated EMI noise caused by MOSFET switching. The AP64102Q also features Frequency Spread Spectrum (FSS) with a switching frequency jitter of $\pm 6\%$, which reduces EMI by not allowing emitted energy to stay in any one frequency for a significant period of time.

The device is available in an SO-8EP package.

- Low-Dropout (LDO) Mode
- Precision Enable Threshold to adjust UVLO
- Protection Circuitry
 - Undervoltage Lockout (UVLO)
 - Output Overvoltage Protection (OVP)
 - Cycle-by-Cycle Peak Current Limit
 - o Thermal Shutdown
- Totally Lead-Free & Fully RoHS
 Compliant
- Halogen and Antimony Free. "Green"
 Device



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APPLICATIONS

- Automotive Power Systems
- Automotive Infotainment
- Automotive Instrument Clusters
- Automotive Telematics
- Automotive Lighting Control
- Advanced Driver Assistance Systems

TYPICAL APPLICATIONS CIRCUIT

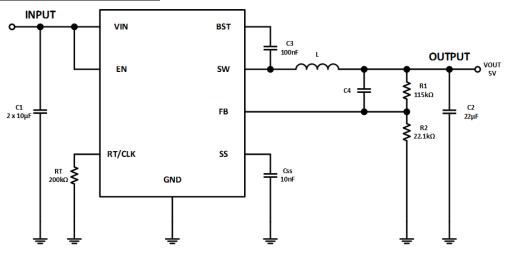


Figure 1. Typical Application Circuit

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Rating	Unit	
VIN	Supply Pin Voltage	-0.3 to +42.0 (DC)	V	
VIIN		-0.3 to +45.0 (400ms)	v	
VBST	Bootstrap Pin Voltage	Vsw - 0.3 to Vsw + 6.0	V	
Ven	Enable/UVLO Pin Voltage -0.3 to +42.0		V	
Vrt/clk	RT/CLK Pin Voltage	-0.3 to +6.0	V	
Vfb	Feedback Voltage	-0.3V to +6.0	V	
Vss	Soft-Start Pin Voltage	-0.3 to +6.0	V	
Vsw	Switch Node Voltage	-0.3 to VIN + 0.3 (DC)	v	
VSW	Switch Node Voltage	-2.5 to VIN + 2.0 (20ns)	V	
TJ	Junction Temperature	+160	°C	
T∟	Lead Temperature	+260	°C	



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RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
VIN	Supply Voltage	3.8	40	V
VOUT	Output Voltage	0.8	39	V
TA	Operating Ambient Temperature Range	-40	+125	°C
TJ	Operating Junction Temperature Range	-40	+150	°C

EVALUATION BOARD

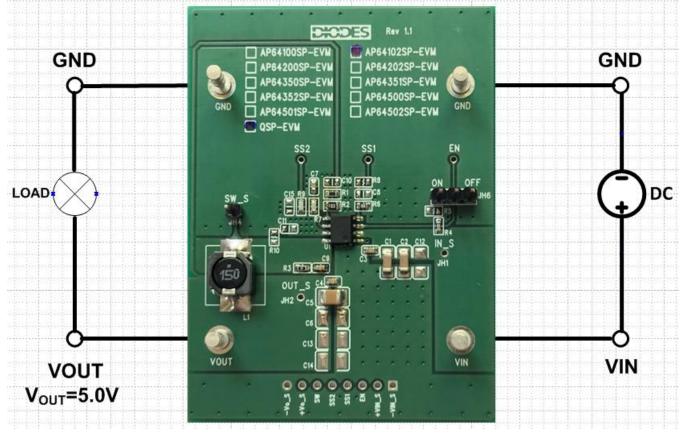


Figure 2. AP64102QSP-EVM



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QUICK START GUIDE

The AP64102QSP-EVM has a simple layout and allows access to the appropriate signals through test points. To evaluate the performance of the AP64102QSP, follow the procedure below:

- 1. Connect a power supply to the input terminals VIN and GND. Set VIN to 12V.
- 2. Connect the positive terminal of the electronic load to VOUT and negative terminal to GND.
- 3. For Enable, to enable IC, place a jumper at JH6 to "ON" position to connect EN pin to VIN through 100KΩ resistor or leave it OPEN. Jump to "OFF" position to disable IC.
- 4. The evaluation board should now power up with a 5.0V output voltage.
- Check for the proper output voltage of 5.0V (±1%) at the output terminals VOUT and GND. Measurement can also be done with a multimeter with the positive and negative leads between VOUT and GND.
- 6. Set the load to 1A through the electronic load. Check for the stable operation of the SW signal on the oscilloscope. Measure the switching frequency.

MEASUREMENT/PERFORMANCE GUIDELINES:

- 1) When measuring the output voltage ripple, maintain the shortest possible ground lengths on the oscilloscope probe. Long ground leads can erroneously inject high frequency noise into the measured ripple.
- 2) For efficiency measurements, connect an ammeter in series with the input supply to measure the input current. Connect an electronic load to the output for output current.

SETTING OUTPUT VOLTAGE:

Table 1 shows a list of recommended component selections for common output voltages.

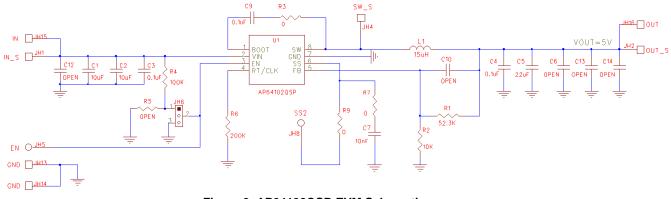
VOUT	R1	R2	L1	C1, C2	C5
1.2V	4.99ΚΩ	10KΩ	6.8µH	2x10µF	22µF
1.5V	8.66KΩ	10KΩ	8.2µH	2x10µF	22µF
1.8V	12.4KΩ	10KΩ	10µH	2x10µF	22µF
2.5V	21.5KΩ	10KΩ	10µH	2x10µF	22µF
3.3V	31.6KΩ	10KΩ	15µH	2x10µF	22µF
5.0V	52.3KΩ	10KΩ	15µH	2x10µF	22µF
12V	140KΩ	10KΩ	33µH	2x10µF	22µF

 Table 1. Common Output Voltages



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EVALUATION BOARD SCHEMATIC





PCB TOP LAYOUT

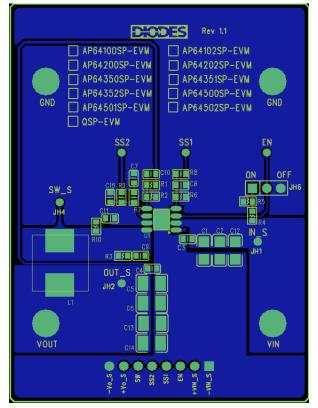


Figure 4. AP64102QSP-EVM – Top Layer



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PCB BOTTOM LAYOUT

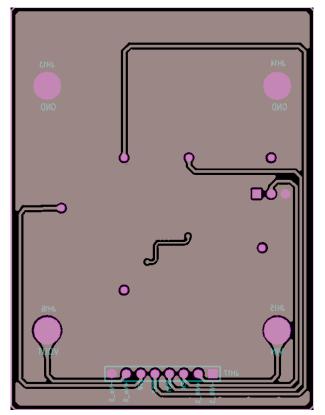


Figure 5. AP64102QSP-EVM – Bottom Layer

BILL OF MATERIALS for AP64102QSP-EVM for Vout=5V

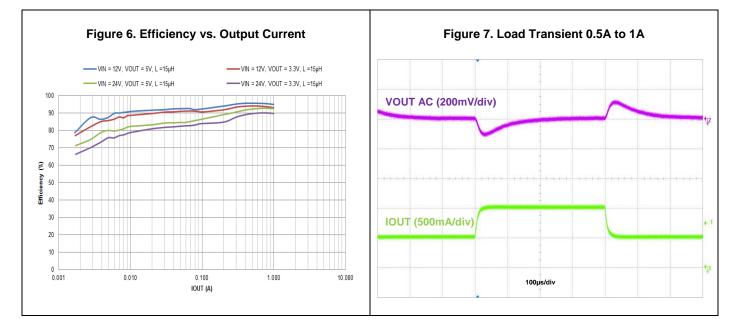
Ref	Value	Description	Qty	Size	Vendor Name	Manufacturer PN
C1, C2	10µF	Ceramic Capacitor, 50V	2	1206	TDK	CGA5L1X7R1H106K160AC
C3, C4, C9	0.1µF	Ceramic Capacitor, 50V	3	0603	TDK	CGA3E3X8R1H104K080AB
C5	22µF	Ceramic Capacitor, 16V	1	1210	TDK	CGA6P1X8L1C226M250AC
C7	10nF	Ceramic Capacitor, 50V	1	0603	TDK	CGA3E2NP01H103J080AA
R1	52.3KΩ	SMD Resistor, 1%	1	0603	Panasonic	ERJ-3EKF5232V
R2	10ΚΩ	SMD Resistor, 1%	1	0603	Panasonic	ERJ-3EKF1002V
R3, R7, R9	0Ω	SMD Resistor, 1%	3	0603	Panasonic	ERJ-3GEY0R00V



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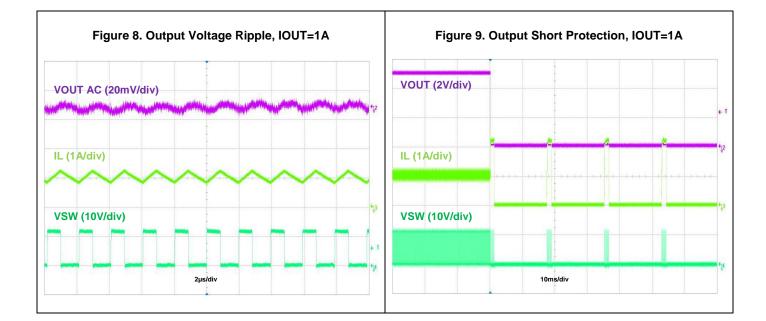
R4	100ΚΩ	SMD Resistor, 1%	1	0603	Panasonic	ERJ-3EKF1003V
R6	200ΚΩ	SMD Resistor, 1%	1	0603	Panasonic	ERJ-S03F2003V
L1	15µH	DCR=69.5mΩ, Ir=2.2A	1	7.4x 7.3x 4.5mm	Wurth Electronics	7447773150
JH6		PCB Header, 36 POS	1	1X3	Amphenol	78511-136HLF
JH13, JH14, JH15, JH16	1598	Terminal Turret Triple 0.094" L (Test Points)	4	Through- Hole	Keystone Electronics	1598-2
U1	AP64102Q	Sync DC-DC Converter	1	SO-8EP	Diodes Incorporated (Diodes)	AP64102QSP

TYPICAL PERFORMANCE CHARACTERISTICS





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