AP3012K
EVB User Guide

1.5MHz, Step-Up DC/DC Converter

DESCRIPTION

The AP3012 is a high power, constant frequency, current mode PWM, inductor based, step-up (boost) converter. The converter operates at high frequency (1.5MHz) so that a small, low profile inductor can be used.

The AP3012 has built-in overvoltage protection (OVP) to allow the device to go into shutdown mode when the output voltage exceeds the OVP threshold of 29V.

The AP3012 is available in standard SOT25 package.

FEATURES

- High Efficiency up to 81%
- Adjustable Output Voltage up to 29V
- Shutdown Current 1μA Typical
- 1.5MHz Switching Frequency
- 36V 500mA Rugged Integrated Bipolar Switch
- Built-in Soft-start to Reduce Inrush Current During Start-up
- On-chip Overvoltage Protection

APPLICATIONS

- LCD/OLED Display Bias Supply
- White LED Driver for LCD Display Backlights
- Cellular Phones

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
VIN	Input Voltage	2.6	16	V
T_{OP}	Operating Temperature	-40	85	°C

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Rating	Unit
V_{IN}	Input Voltage	20	V
—	SW Voltage	38	V
—	FB Voltage	5	V
—	$\overline{SHDN}$ Voltage	16	V
θ_{JA}	Thermal Resistance (Junction to Ambient, no Heat Sink)	265	°C/W
—	Operating Junction Temperature	+150	°C
T_{STG}	Storage Temperature Range	-65 to +150	°C
T_{LEAD}	Lead Temperature (Soldering, 10sec)	+260	°C
—	ESD (Machine Model)	250	V
—	ESD (Human Body Model)	2000	V

EVALUATION BOARD

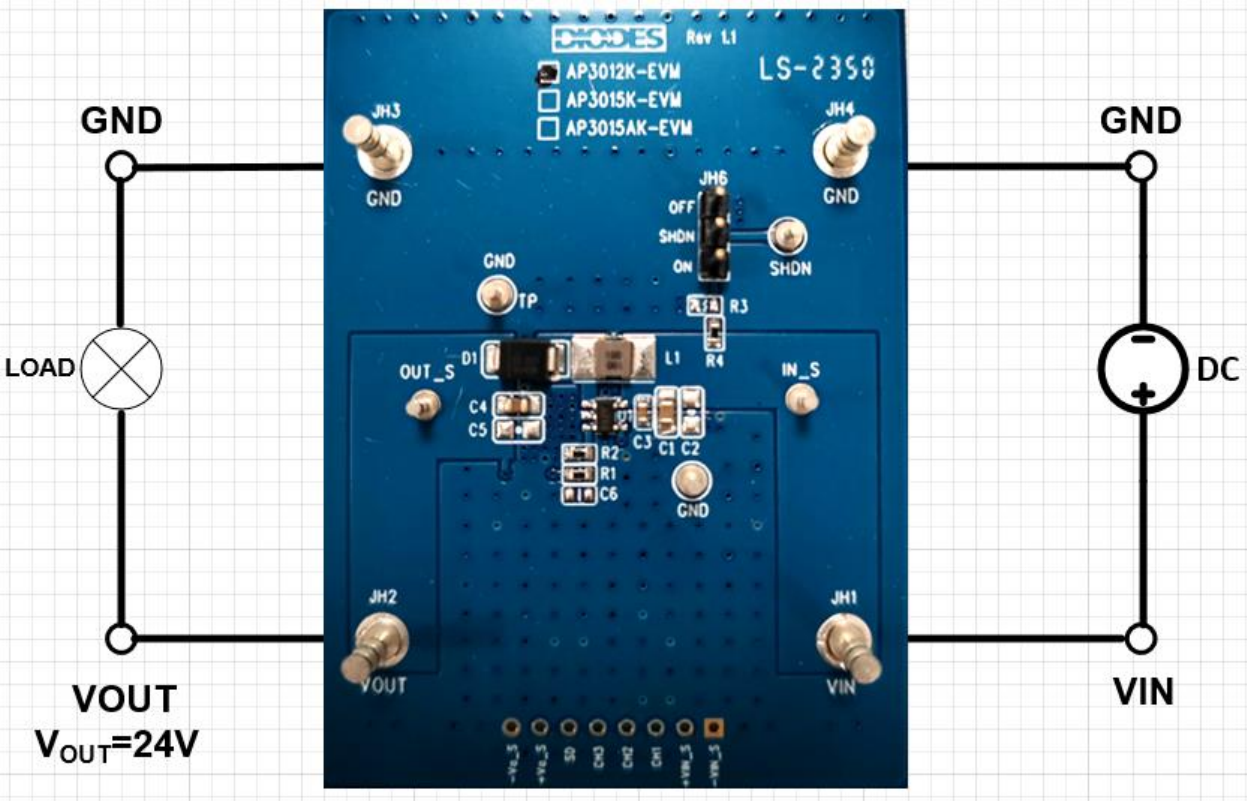


Figure 1. AP3012K-EVM

QUICK START GUIDE

The AP3012K-EVM has a simple layout and allows access to the appropriate signals through test points. To evaluate the performance of the AP3012KTR, follow the procedure below:

1. Connect a power supply to the input terminals VIN and GND. Set VIN to 5V.
2. Connect the positive terminal of the electronic load to VOUT and negative terminal to GND.
3. For Enable, to enable IC, place a jumper at JH6 to “ON” position to connect EN pin to VIN through 100KΩ resistor. Jump to “OFF” position to disable IC.
4. The evaluation board should now power up with a 24V output voltage.
5. Check for the proper output voltage of 24V at the output terminals VOUT and GND. Measurement can also be done with a multimeter with the positive and negative leads between VOUT_S and GND.
6. Set the load to 30mA through the electronic load. Check for the stable operation of the SW signal on the oscilloscope. Measure the switching frequency.

MEASUREMENT/PERFORMANCE GUIDELINES:

1) When measuring the output voltage ripple, maintain the shortest possible ground lengths on the oscilloscope probe. Long ground leads can erroneously inject high-frequency noise into the measured ripple.

2) For efficiency measurements, connect an ammeter in series with the input supply to measure the input current. Connect an electronic load to the output for output current.

EVALUATION BOARD SCHEMATIC

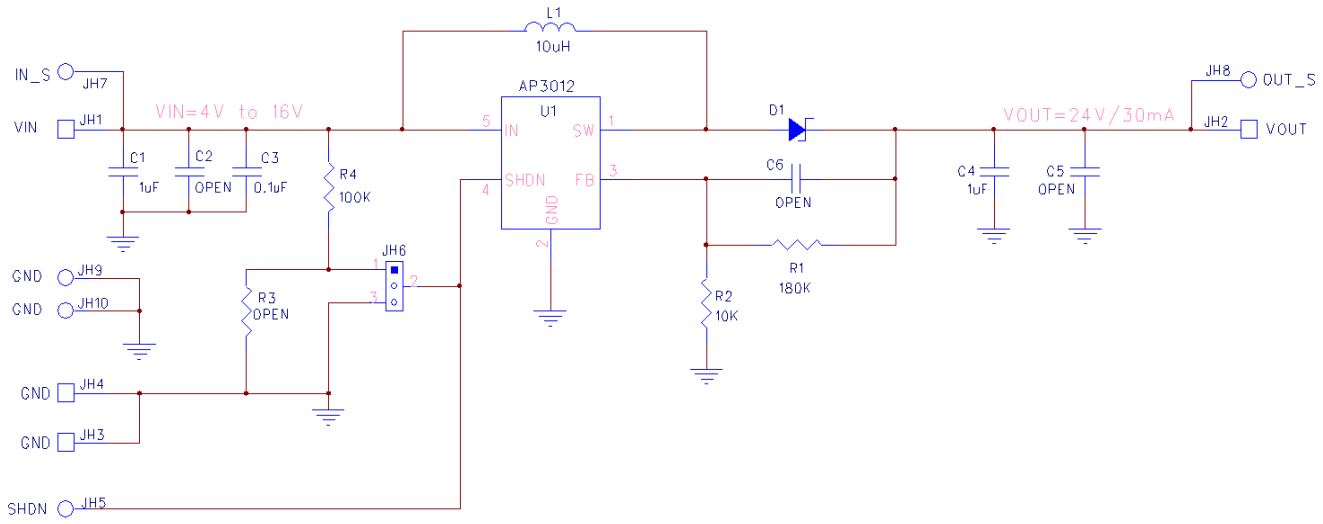
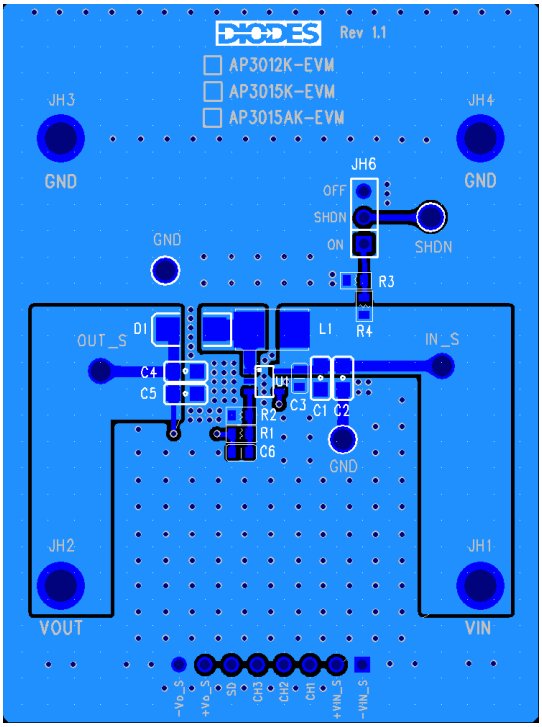
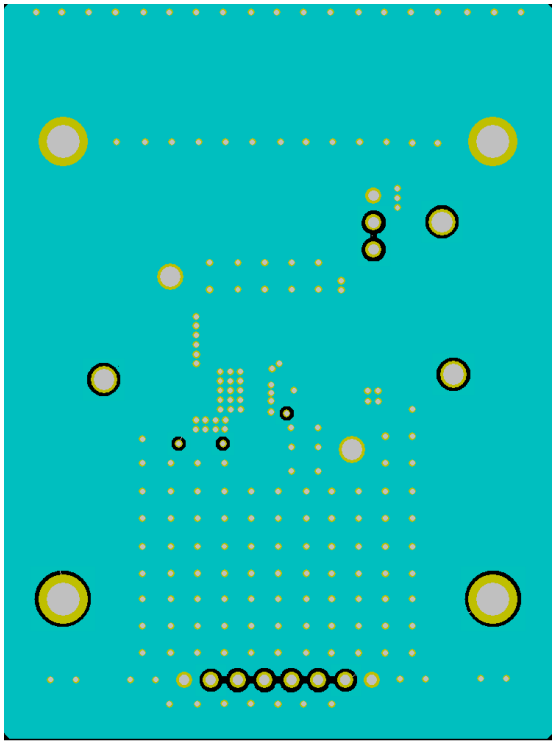


Figure 2. AP3012K-EVM Schematic

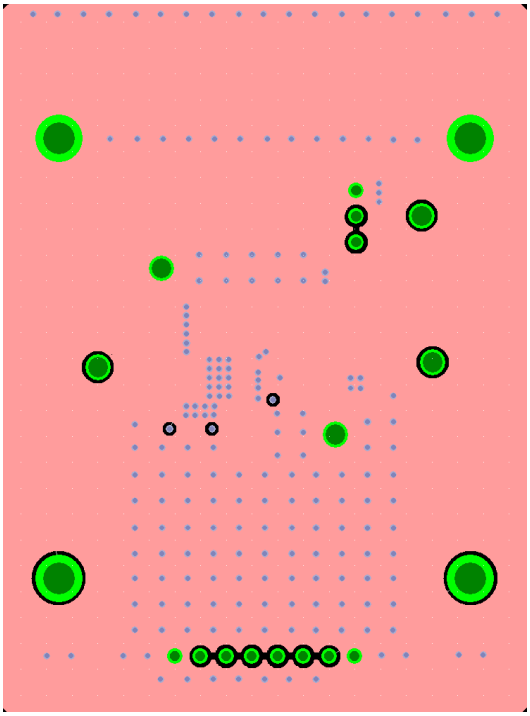
TOP LAYOUT



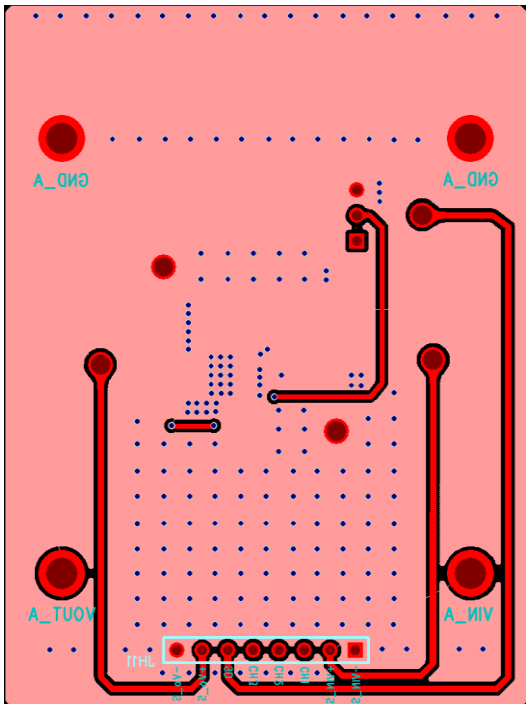
INNER LAYER 2 LAYOUT



INNER LAYER 3 LAYOUT



BOTTOM LAYOUT



BILL OF MATERIALS (BOM) for AP3012K-EVM

Ref	Value	Description	Qty	Size	Vendor Name	Manufacturer PN
C1	1µF	Ceramic Capacitor, 25V, X7R, 10%	1	0805	Murata	GCM21BR71E105KA56L
C3	0.1µF	Ceramic Capacitor, 25V, X7R, 10%	1	0603	Murata	GCJ188R71E104KA12D
C4	1µF	Ceramic Capacitor, 35V, X7R, 10%	1	0805	Murata	GCM21BR7YA105KA55L
R4	100KΩ	RES SMD 1%, 1/10W	1	0603	Panasonic	ERJ-3EKF1003V
R1	180KΩ	RES SMD 1%, 1/10W	1	0603	Panasonic	ERJ-3EKF1803V
R2	10KΩ	RES SMD 1%, 1/10W	1	0603	Panasonic	ERJ-3EKF1002V
L1	10µH	DCR=322mΩ, Ir=1.65A	1	3.0x3.0x2.0 mm	Wurth Electronics	74438336100
D1		Diode Schottky 40V, 1A	1	SMB	Diodes Incorporated (Diodes)	B140B-13-F
JH6		PCB Header, 40 POS	1	1x3	Wurth Electronics	61304011121
IN_S, OUT_S, SHDN, GNDx2	1573	Terminal Turret 0.082" L (Test Points)	5	Through-Hole	Keystone Electronics	1573-2
JH1, JH2, JH3, JH4	1598	Terminal Turret Triple 0.094" L (Test Points)	4	Through-Hole	Keystone Electronics	1598-2
U1	AP3012	Sync DC/DC Converter	1	SOT25	Diodes	AP3012KTR

TYPICAL PERFORMANCE CHARACTERISTICS

Figure 3. Efficiency vs. Output Current

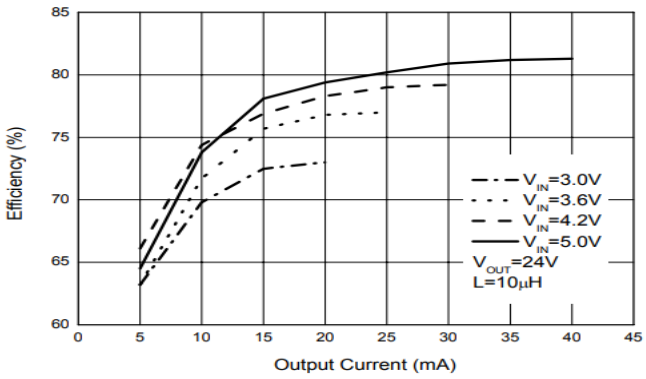


Figure 4. Efficiency vs. Junction Temp

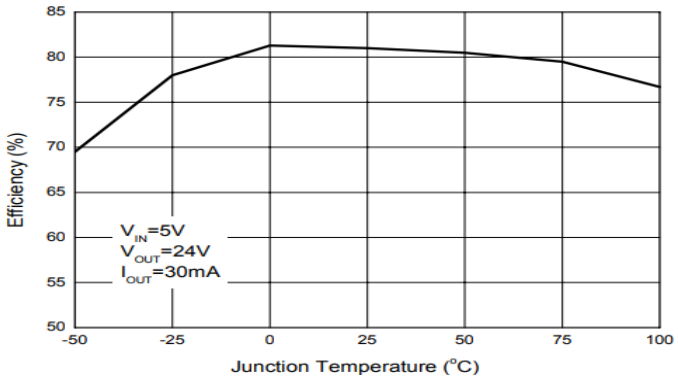


Figure 5. FSW vs. Junction Temp

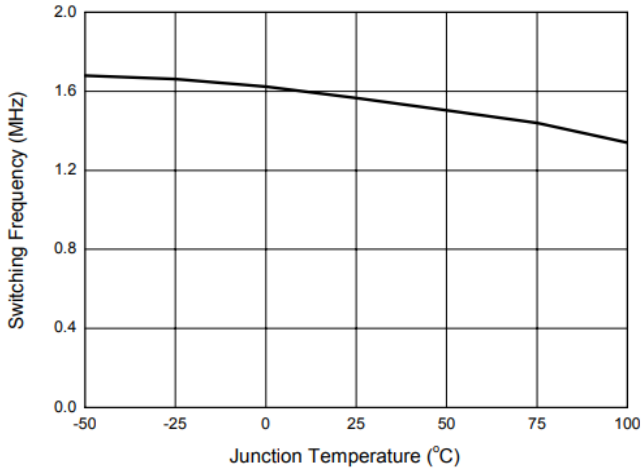
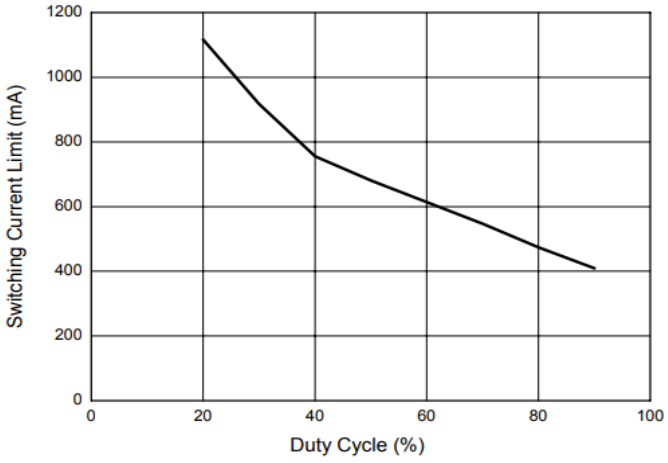


Figure 6. ILIMIT-SWITCH vs. Duty Cycle



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